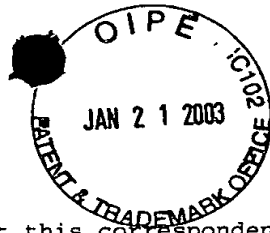


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By: Am p 2

Date: 1/16/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Albrecht Mayer

Applic. No. : 09/164,123

Filed : September 30, 1998

Title : Method for Assembling Integrated Circuits
With Protection Of The Circuits Against
Electrostatic Discharge, And Arrangement Of
Integrated Circuits With Protection Against
Electrostatic Discharge

Examiner : Julio J. Maldonado

Group Art Unit : 2823

21/D(4)
FJONES
1-24-03

do not enter
01/28/2003 flr
3/11/03
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A M E N D M E N T under 37 C.F.R. § 1.116

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

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S i r :

Responsive to the final Office action dated November 6, 2002

kindly amend the above-identified application as follows:

In the Claims:

Claim 1 (twice-amended). A method for producing an electrical
connection between integrated circuit chips, which comprises:

D'